

10/10/02  
JC 3712

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
100911193	03/05/2002	257		2636 2811	Tian, Tan

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\*\*CONTINUING DATA VERIFIED.

THIS APPLICATION IS A CIP OF 09/579,044 05/26/2000

yes / / T

\*\*FOREIGN APPLICATIONS VERIFIED: no / / T

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no
Verified and Acknowledged Examiner's initials	TT
ATTORNEY DOCKET NO YOR91999-0420-US2	
TITLE : Semiconductor high dielectric constant decoupling capacitor structures and process for fabrication	
5010-100-COMM-PAT-5 1M PTO 476, Rev. 12-94	

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Draw.	Fig. Draw.	Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE		
		Application Examiner		
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